Toyama College		Year	Year 2022		Course Title	Computer system II#				
Course	Informa	tion	•	•						
Course Code 0056						•	d / Elective			
Class Format Lecture			nent of Electronics	ent of Electronics and Computer			School Credit: 1			
Department Engineering			ring				2nd			
Term Textbook	and/or	Second	Semester	emester Classes per W			eek 2			
	Materials	飯高成男	「ディジタル回路	「ディジタル回路の計算」(オーム社)						
Instructo	_		Junko,Akiguchi Sh	nunsuke						
At the co 1) Config	ure the log	of this cours	se, students will be f a synchronous ce ecoder logic circuit	ounter.						
Rubric										
			Ideal Level				Unacceptable Level			
Flip flop				Can understand and explain the operation of flip-flops almost perfectly.		and explain th flops correctly				
Counter			operation of ty	Can understand and design the operation of typical sequential circuits such as counters almost perfectly.		and design the cal sequential counters	Can't understand and design the operation of typical sequential circuits such as counters correctly.			
Encoder /	/ decoder		operation of ty logic circuits su	d and design the rpical combination uch as encoders almost perfectly.	Can understand and design the operation of typical combination logic circuits such as encoders and decoders correctly.		Can't understand and design the operation of typical combination logic circuits such as encoders and decoders correctly.			
Assigne	d Depar	tment Ol	ojectives		•					
Teachir	ng Metho	od								
Outline		Logic cir	cuits can be said a digital circuits, a	to be an introducti Ind acquire ideas a	ion to computer hand technologies th	ardware. Deve	elop the ability to operate and lied to complex digital circuits.			
Style			s led by teacher.				μ			
Notice			-	requires 50 points	or more rating.					
Charact	teristics	of Class /	<u>Division in Le</u>		1		☑ Instructor Professionally			
☑ Active	Learning		☑ Aided by IC	T	☐ Applicable to	Remote Class	Experienced			
Course	Plan									
00000			Theme		G	oals				
	3rd Quarter	1st	Syllabus descripti RS flip flop	yllabus description. S flip flop			Can explain the operation of RS flip-flops and synchronous RS flip-flops.			
		2nd	Master-slave RS 1	laster-slave RS flip-flop			Can explain the operation of the master-slave RS flip-flop.			
2nd Semeste r		3rd	JK flip flop	( flip flop			Can explain the operation of JK flip-flops and master-slave JK flip-flops.			
		4th	D flip-flop, T flip-	flip-flop, T flip-flop			Can explain the operation of D flip-flops and T flip-flops.			
		5th	Sequential logic o	equential logic circuit			Can explain the concept of sequential logic circuits consisting of flip-flops and Combinational logic circuits.			
		6th	Asynchronous bir	synchronous binary counter			Can explain the behavior of asynchronous binary counters.			
		7th	Asynchronous n-	synchronous n-ary counter			Can explain the behavior of asynchronous n-ary counters.			
		8th	Semester midteri	emester midterm exam.			Midterm examination.  Measure the ability to analyze sequential circuits and the degree of understanding of flip-flop operation.			
	4th Quarter	9th	Synchronous bina	ynchronous binary counter			Can explain the behavior of synchronous binary counters.			
		10th	Synchronous dec	ynchronous decimal counter			Can explain the operation of the synchronous decimal counter.			
		11th	Synchronous n-a	ynchronous n-ary counter			Can explain the operation of the synchronous nary counter.			
		12th	Shift register	hift register			Can explain the operation of the shift register.			
		13th	Encoder and deco	ncoder and decoder			Can explain how the encoder works. Can explain how the decoder works.			
						Can explain the concept of the various input and output scheme used in digital circuits.				
		14th	Various input / o	utput methods	C	an explain the utput scheme	concept of the various input and used in digital circuits.			

	16th F	Return and explanation of the final exam.			Return of the final exam.			
Evaluation N	Method and W	eight (%)						
	Midterm exam	Final exam	Submissions.	Behavior	Portfolio	Other	Total	
Subtotal	35	35	30	0	0	0	100	
Basic Proficiency	35	35	30	0	0	0	100	
Specialized Proficiency	0	0	0	0	0	0	0	
Cross Area Proficiency	0	0	0	0	0	0	0	