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		☐ Applicable to F	emote Class	☐ Instructor Pre Experienced	ofessionally		
Course Plan							
	Theme	als					
	Syllabus description. History of the		Can explain the history of computers.				
2-4	Data Words	Can explain how to represent a decimal digit.					
	Number representation: decimal of Number representation: binary did	Can explain how to represent a binary digit.					
	Arithmetic shift, code extension, ar	Can explain arithmetic shifts, code extensions,					
3rd 4th Quarter	calculation.	and address calculations.					
5th	Floating point number.	Can explain floating point numbers.					
6th	An instruction word configuration (	Can explain instructions and operands.					
7th	An instruction word configuration (	Can explain type of instruction and addressing format.					
2nd 8th	Semester mid-term exam.		Midterm examination.				
Semeste Oth	Arithmetic circuit configuration: su	rithmetic circuit configuration : summing			Can explain binary adder.		
10th	Arithmetic circuit configuration: m circuit.	Can explain binary multiplier.					
11th	Arithmetic circuit configuration: pi	Can explain the arithmetic pipeline process.					
4th 12th	Execution control of instructions.	Can explain the execution control of an instruction.					
Quarter 13th	Memory system configuration.		Can explain how to make memory systems faster				
	Configuration of input / output sys	tem Ca	Can explain the control of the input / output device.				
15th	Semester final exam.	Terminal examination.					
	Return and explanation of the final		Return of the final exam.				
Evaluation Method and V	•	- 1100	2. 3. 9 111				
Midterm exam		la		Other	Total		
Subtotal 25		I Behavior	Orttolio		LIUIAI		
Basic Ability 25	50 25	Behavior P	ortfolio	0	100		

Technical Ability	0	0	0	0	0	0	0
Interdisciplinar y Ability	0	0	0	0	0	0	0