Akashi College		Year	ear 2022		Course Title			
Course	Informa	tion						
Course C	Course Code 4426					/ Specialize	ed / Compulsory	
Class For	ass Format Experiment					School C	edit: 4	
Departme	epartment Electrical and Com Electrical Engineer			Computer Engineering neering Course		4th		
Term		Year-roun	<u> </u>	Classes per		4		
Textbook	and/or Materials							
Instructo		KAMI Yası	shi.HIROTA Ats	sushi,TERASAWA S	Shinichi, HIRANO I	Masatsugu.NON	IURA Havato	
	Objectiv				,		- 7	
1. Can ac 2. Can co	ctively part	icipate in exp eriments in a	planned manne	oup and carry out our based on the ba	sic ability, and ar	nalyze the resul	the group members. ts of an experiment.	
Rubric				•	<u> </u>			
1.43110			Ideal Level		Standard Level Unacceptable Level			
Achievement 1			Can actively pa	group and carry ts in cooperation	Can carry out experiments in cooperation with the group members.		Cannot carry out experiments.	
Achievement 2				xperiments in a er and analyze	Can analyze the results of the experiments.		Cannot analyze the results of an experiment.	
Achievement 3			Can summarize the results of an experiment in a report with correct writing expressions and submit in time.  Can summarize an experiment ir correct writing expressions and submit in time.		n a report with	Cannot summarize the results of an experiment in a report.		
Assigne	ed Depar	tment Obje	ectives					
Teachir	ng Metho	d						
Style Notice		power circ be supervi Students v measurem They will a guidance p	ment circuits; Kami, controls; Nomura and Terazawa, circuits and microcomputers; and Hirota, reuits. The experiments in weeks 2 to 5 of the first semester and week 4 of the second semester will vised by persons engaged in the development of electronic devices and other activities in a company. It will conduct experiments on themes closely related to the electrical and electronic fields, such as ment, circuits, control, and microcomputers, in groups of four to five, and submit a report on them. It is actively conduct experiments give, based on their own necessary preparation and pre-study, and exprovided on the spot by the instructor of the experiment.  Forts have not been received by the due date, students will not receive a passing grade. Students and put away the equipment. Precautions regarding the experiments will be given during week of the first and second semesters. Students have to participate in all experiments.					
		the first w	eek of the first a	t away the equipment of the second se	nent. Precautions ters. Students ha	regarding the	experiments will be given during e in all experiments.	
Cl	!	the first w Students v	eek of the first a vill not be grade	it away the equipn and second semes ad unless they hav	nent. Precautions ters. Students ha	regarding the	experiments will be given during e in all experiments.	
Charact	teristics	the first w Students v	eek of the first a	it away the equipn and second semes ad unless they hav	nent. Precautions ters. Students ha	regarding the	experiments will be given during e in all experiments.	
	teristics (	the first w Students v	eek of the first a vill not be grade	t away the equipn and second semes ad unless they hav arning	nent. Precautions ters. Students ha	regarding the ve to participat all experiments	experiments will be given during e in all experiments.	
	e Learning	the first w Students v	eek of the first a vill not be grade Division in Le	t away the equipn and second semes ad unless they hav arning	nent. Precautions ters. Students ha e participated in	regarding the ve to participat all experiments	experiments will be given during e in all experiments.  .  Instructor Professionally	
□ Active	e Learning	the first w Students v of Class / D	eek of the first a vill not be grade Division in Le	t away the equipn and second semes ad unless they hav arning	nent. Precautions ters. Students had a participated in  Applicable to	regarding the ve to participat all experiments  Remote Class  Goals	experiments will be given during e in all experiments.  Instructor Professionally Experienced	
□ Active	e Learning	the first w Students v of Class / D	eek of the first a vill not be grade Division in Le  Aided by IC	it away the equipn and second semes ad unless they have arning	nent. Precautions ters. Students had a participated in  Applicable to	regarding the ve to participat all experiments  Remote Class  Goals  Juderstand the engineering experiments experiments	experiments will be given during e in all experiments.  Instructor Professionally Experienced  various precautions related to eriments and the outline of the	
□ Active	e Learning	the first w Students v of Class / E	eek of the first a vill not be grade Division in Le  Aided by IC	and second semested unless they have arning	nent. Precautions ters. Students had a participated in  Applicable to	regarding the ve to participat all experiments  Remote Class  Goals  Juderstand the engineering expheme of each education of ed	experiments will be given during e in all experiments.  Instructor Professionally Experienced  various precautions related to eriments and the outline of the	
□ Active	e Learning	the first w Students v of Class / E	eek of the first a vill not be grade Division in Le  Aided by IC  meme  speriment guida	art away the equipn and second semes and unless they have arning T	nent. Precautions ters. Students had a participated in  Applicable to	regarding the ve to participat all experiments  Remote Class  Goals  Juderstand the engineering expheme of each educate Juderstand logical circuits upon the pogical circuits upon the control of the con	experiments will be given during e in all experiments.  Instructor Professionally Experienced  various precautions related to eriments and the outline of the experiment.  c circuit inputs using IDE elopment Environment).  simulation and debugging of sing the IDE (Integrated	
□ Active	Plan	the first w Students v of Class / E	eek of the first avill not be grade Division in Le  Aided by IC  Demonstrate a service of the property of the	art away the equipn and second semes and unless they have arning T	nent. Precautions ters. Students have participated in  Applicable to	regarding the ve to participat all experiments  Remote Class  Goals  Jinderstand the engineering expense of each educate of last and logical circuits understand the ogical circuits understand the operations of the control of the co	experiments will be given during e in all experiments.  Instructor Professionally Experienced  various precautions related to eriments and the outline of the experiment.  c circuit inputs using IDE elopment Environment).  simulation and debugging of sing the IDE (Integrated environment).  uit implementation in FPGA(Field	
□ Active	e Learning	the first w Students v of Class / E	eek of the first avill not be grade Division in Le  Aided by IC  Demonstrate a service of the property of the	art away the equipment second semested unless they have arning  T  Ince  esign)  debug)	nent. Precautions ters. Students have participated in  Applicable to	regarding the ve to participat all experiments  Remote Class  Goals  Inderstand the engineering expheme of each of the last and logical circuits understand the opical circuits understand	experiments will be given during e in all experiments.  Instructor Professionally Experienced  various precautions related to eriments and the outline of the experiment.  c circuit inputs using IDE elopment Environment).  simulation and debugging of sing the IDE (Integrated environment).  uit implementation in FPGA(Field	
□ Active	Plan lst	the first w Students v of Class / E	eek of the first avill not be grade Division in Le Aided by IC  PGA1 (Circuit de PGA2 (emulator  PGA3 (implemer	art away the equipned and second semested unless they have arning  T  Ince  esign)  debug)  atation and operation)	nent. Precautions ters. Students have participated in  Applicable to	regarding the ve to participat all experiments  Remote Class  Goals  Understand the engineering expense of each of the color of the col	experiments will be given during e in all experiments.  Instructor Professionally Experienced  various precautions related to eriments and the outline of the experiment. c circuit inputs using IDE elopment Environment). simulation and debugging of sing the IDE (Integrated avironment). uit implementation in FPGA(Field Logic Array). operation, debugging, and plementation circuitry with	
Course  1st Semeste	Plan lst	the first w Students v of Class / E	eek of the first a vill not be grade Division in Le Aided by IC  PGA1 (Circuit de PGA2 (emulator PGA3 (implement) PGA4 (evaluation	art away the equipment second semested unless they have arning  T  Ince  Ince  Institute of the series of the seri	nent. Precautions ters. Students had a participated in  Applicable to	Remote Class  Goals  Jinderstand the engineering expheme of each of the logical circuits understand the ogical circuits understand the logical circuits unders	experiments will be given during e in all experiments.  Instructor Professionally Experienced  various precautions related to eriments and the outline of the experiment. c circuit inputs using IDE elopment Environment). simulation and debugging of sing the IDE (Integrated avironment). uit implementation in FPGA(Field Logic Array). operation, debugging, and plementation circuitry with	
Course  1st Semeste	Plan lst	the first w Students v of Class / E	eek of the first a vill not be grade oill not oill not be grade oi	art away the equipment second semested unless they have arning  T  Ince  Esign)  debug)  Intation and operation	nent. Precautions ters. Students have participated in  Applicable to	Remote Class  Goals  Jinderstand the engineering experiment of each education of each education of integrated Development Engineering expensive support of each education of integrated Development Engineering experiment into Can perform was processing using terface.  Can fabricate a	various precautions related to eriments and the outline of the experiment.  c circuit inputs using IDE elopment Environment).  simulation and debugging of sing the IDE (Integrated invironment).  uit implementation in FPGA(Field logic Array).  operation, debugging, and plementation circuitry with  d compile the results of the a report.  veform measurement and g a computer and measurement	
Course  1st Semeste	Plan lst	the first w Students v of Class / E	eek of the first a vill not be grade vill not be	art away the equipment second semested unless they have arning  T  Ince  Esign)  debug)  Intation and operation	nent. Precautions ters. Students have participated in  Applicable to	Remote Class  Goals  Jinderstand the engineering experiment of each engineering expenses of each experiment into can perform was processing using terface.  Can fabricate a en interface mic can examine are experiment into experiments.	various precautions related to eriments and the outline of the experiment).  various precautions related to eriments and the outline of the experiment.  c circuit inputs using IDE elopment Environment).  simulation and debugging of sing the IDE (Integrated environment).  uit implementation in FPGA(Field elogic Array).  operation, debugging, and plementation circuitry with  d compile the results of the a report.  veform measurement and g a computer and drophone for measurement.  d compile the results of the	

		11th	Direct current voltage stabilization circuit			Can investigate the characteristics of a voltage stable circuit in a rectification circuit.				
		12th	Report organization	Report organization			Can examine and compile the results of the experiment into a report.			
		13th	Oscillation circuits	Oscillation circuits			Can investigate various characteristics for various types of typical oscillation circuits.			
		14th	Low frequency amplifier characteristics			Can examine the circuit operation and characteristics of the push-pull amplifier.				
		15th	Report organization			Can examine and compile the results of the experiment into a report.				
		16th	No final exam							
2nd Semeste r	3rd Ouarter	1st	Experiment guida	Experiment guidance			Understand the various precautions related to engineering experiments and the outline of the theme of each experiment.			
		2nd	Microcomputer co	Microcomputer control I			Can build control systems using embedded microcomputers.			
		3rd	Microcomputer control II			Can build control systems using embedded microcomputers.				
		4th	Microcomputer control III			Can build control systems using embedded microcomputers.				
	Quui toi	5th	Report organization			Can examine and compile the results of the experiment into a report.				
		6th	Transistor amplifi	Transistor amplifier			Can design a transistor amplifier			
		7th	Report organization			Can examine and compile the results of the experiment into a report.				
		8th	Equivalent circuit of the transformer			Can determine the equivalent circuit and constant of the transformer.				
		9th	Report organization	Report organization			Can examine and compile the results of the experiment into a report.			
		10th	Sequence control	Sequence control I			Understand the basics of relay sequence control.			
		11th	Report organization	eport organization			Can examine and compile the results of the experiment into a report.			
	4th Quarter	12th	Sequence control	Sequence control II			Can construct a relay sequence control method of a control circuit that meets the specified specification.			
	Quantities (	13th	Report organization	Report organization			Can examine and compile the results of the experiment into a report.			
		14th		le speed control of the inductive electric by means of a PWM inverter arizing and organizing			Understand the principles of PWM inverters and speed control of inductive electric motors.			
		15th	Summarizing and				Can summarize and organize the experiment.			
		16th	No final exam							
Evaluati	on Met	hod and	Weight (%)		,					
	Re		Presentation	Mutual Evaluations between students	Behavior	Portfolio	Other	Total		
Subtotal 80		)	0	0	20	0	0	100		
Basic Proficiency			0	0	0	0	0	0		
Specialized Proficiency		)	0	0	20	0	0	100		
Cross Area Proficiency			0	0	0	0	0	0		